

All communications respecting this case should identify it by number and names of parties.



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Interference No. 102,725

Applicants: Evans, Jr. et al.
Serial No.: 07/582,672
Filed: 09/14/90
For: Non-Volatile Memory
Circuit Using Ferroelectric
Capacitor Storage Element

Accorded Benefit of: U.S. Serial Nos.
07/440,434 filed 10/20/89 and 07/057,100
filed 06/02/87, now abandoned

The above identified interference is hereby redeclared
by adding the party Schwee:

The cases involved in this interference are:

Junior Party

Applicants: Joseph T. Evans, Jr., William D. Miller and Richard
H. Womack

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Serial No.: 07/582,672 filed 09/14/90

For: Non-Volatile Memory Circuit Using Ferroelectric Capacitor
Storage Element

Assignees: National Semiconductor Corp., A Corp. of DE

Attorneys of Record: D. Carl Richards, William D. Harris, Jr.,
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Dale B. Nixon

Associate Attorney: Lee Patch, Michael A. Glenn and Gail W.
Woodward

Serial No. 07/582,672

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Accorded Benefit of: U.S. Serial Nos. 07/440,434 filed 10/20/89,
07/057,100 filed 06/02/87, now abandoned

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Junior Party

Applicant: Leonard J. Schwee

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Serial No.: 07/575,745 filed 08/31/90

For: Nonvolatile Ferroelectric Memory Cell

Assignees: The United States of America, as represented by the
Secretary of the Navy

Attorneys of Record: Kenneth E. Walden and Jacob Shuster

Associate Attorney: None

Accorded Benefit of: U.S. Serial No. 07/054,973 filed 05/28/87

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Senior Party

Patentee: S. Sheffield Eaton, Jr.

Address: 3361 Spring Ridge Circle, Colorado Springs, CO 80906

Serial No.: 07/013,746 filed 02/12/87, now Patent No. 4,873,664
issued 10/10/89

For: Self Restoring Ferroelectric Memory

Assignees: Ramtron Corporation, Colorado Springs, CO

Attorneys of Record: Ronald L. Wanke, Edward D. Manzo, James D.
Ryndak, Jean M. Dudek and Roger R. Wise

Associate Attorney: None

Accorded Benefit of: None

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Count 1

In a nonvolatile ferroelectric memory of the type having a plurality of memory cells, a bit line coupled to each said memory cell, each said memory cell comprising a ferroelectric capacitor having first and second plate electrodes, the polarization of said capacitors corresponding to the data stored therewithin, the improvement wherein:

said memory further comprising a plurality of word lines and a plurality of plate lines distinct from said bit lines and word lines, each of the memory cells being coupled to a word line, each memory cell being coupled also to a plate line, each plate line being coupled to a capacitor plate electrode of a cell; and

each said memory cell further including a respective switching device located within the memory cell, said first plate electrode of said capacitor in said cell being coupled to one said bit line via said switching device, said switching device being coupled to be controlled by one said word line, said second plate electrode of said capacitor in said cell being coupled to one said plate line.

The claims of the parties which correspond to this count are:

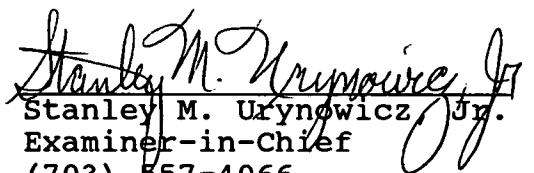
Evans Jr., et al.: Claims 72-74

Schwee : Claims 7 and 9

Eaton, Jr. : Claims 1 and 17

New times have been set as follows:

1. Statements and Motions due APR 13 1992.
2. Identity of lead attorney or agent due JAN 27 1992.


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gjh